

1042-EP

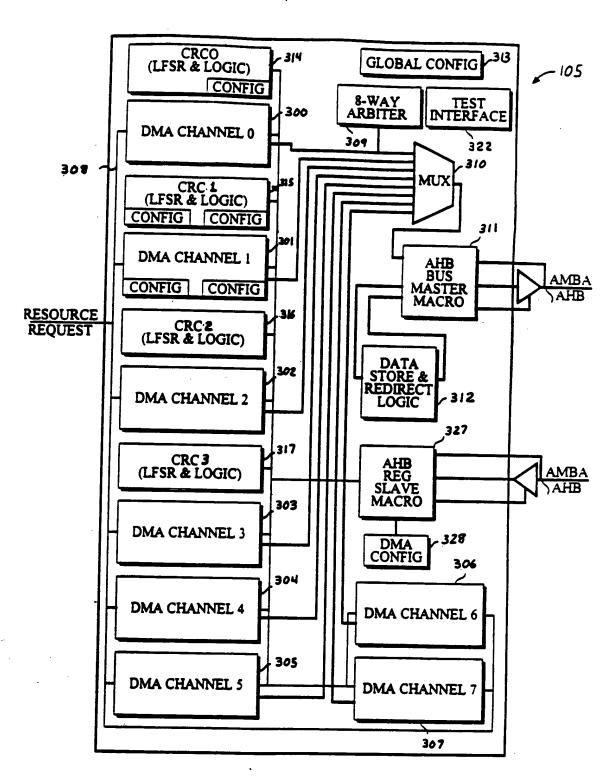
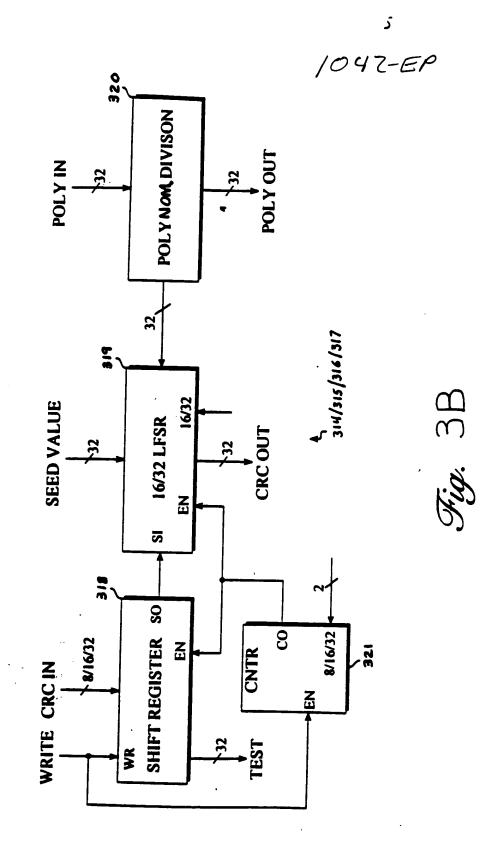


Fig. 3A



1042-EP

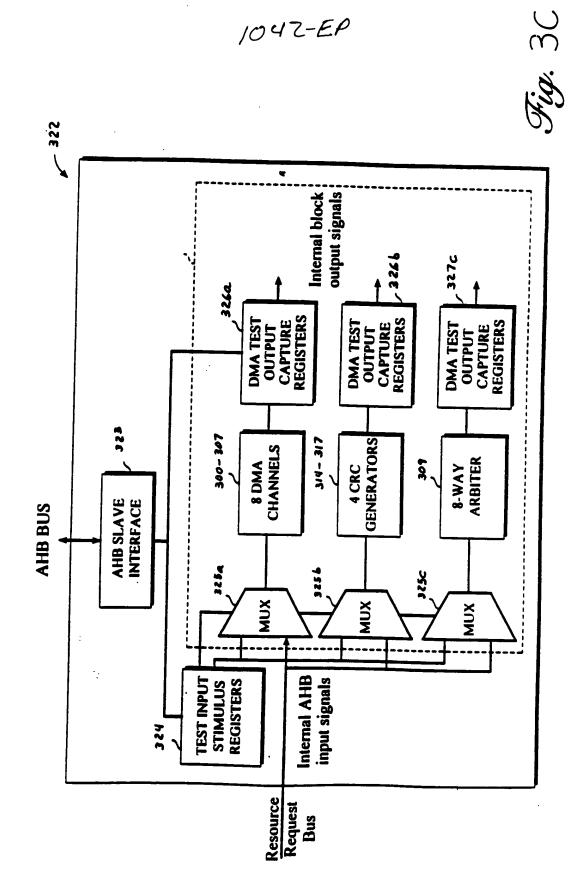
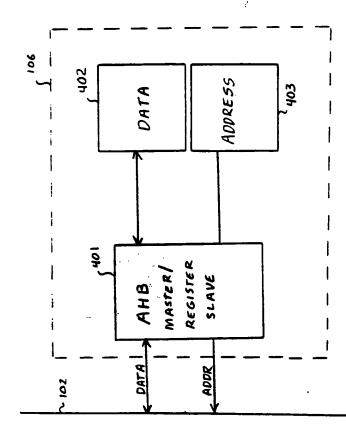


FIG. 4A

1042-EP



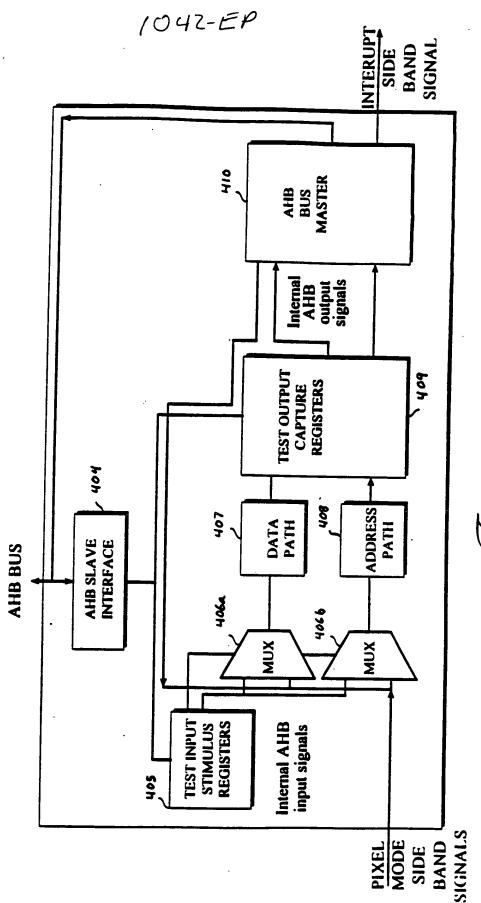
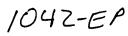
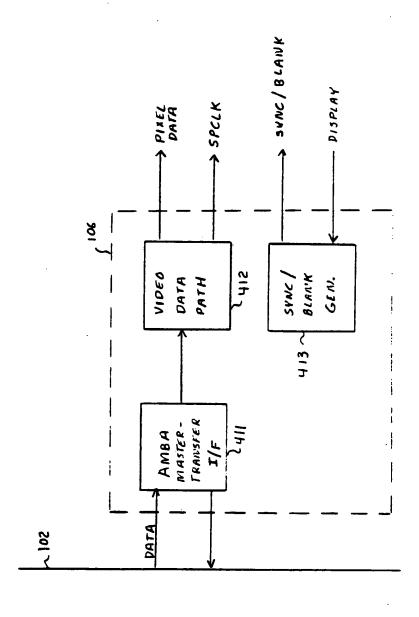


Fig. 4B





F16. 4C

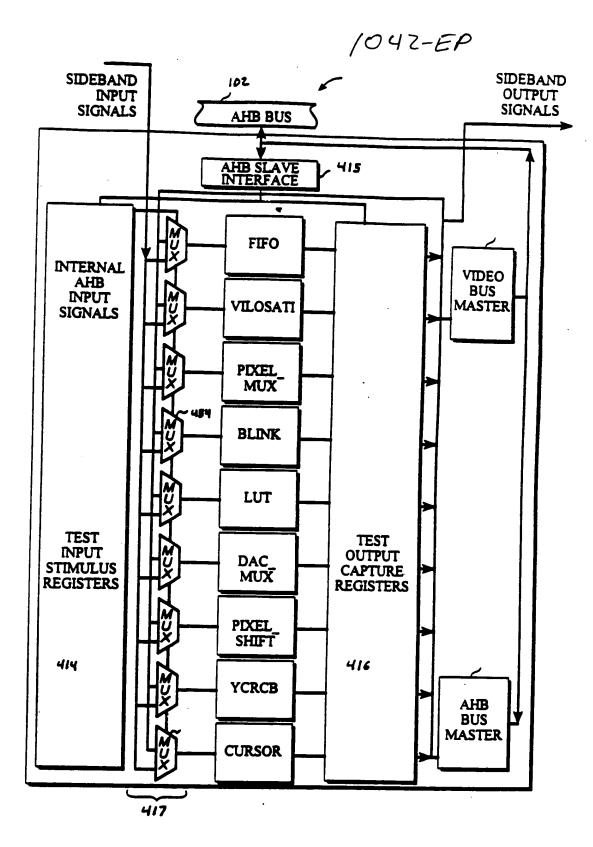
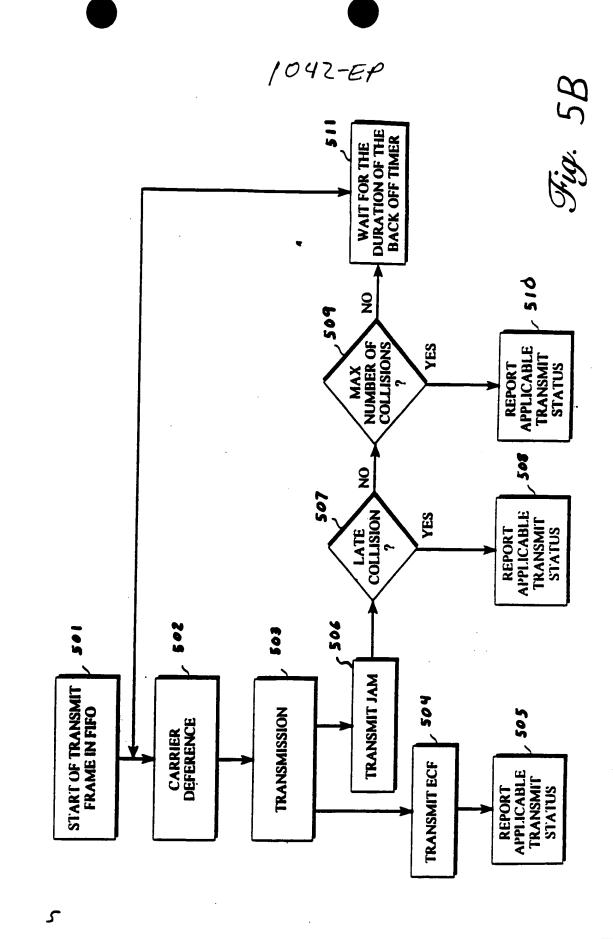
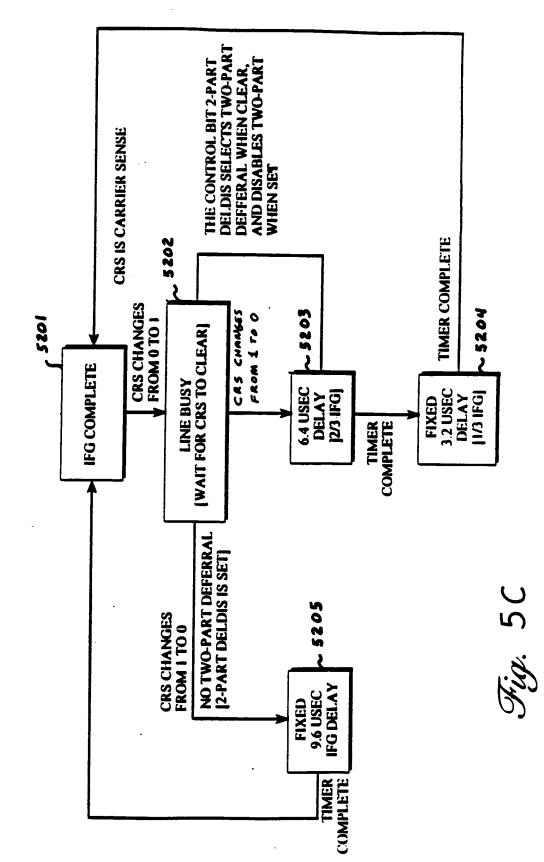


Fig. 40

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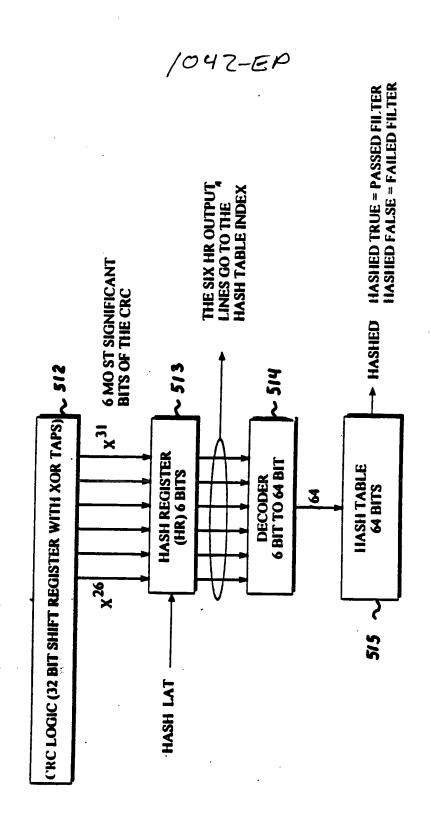


Fig. 5D

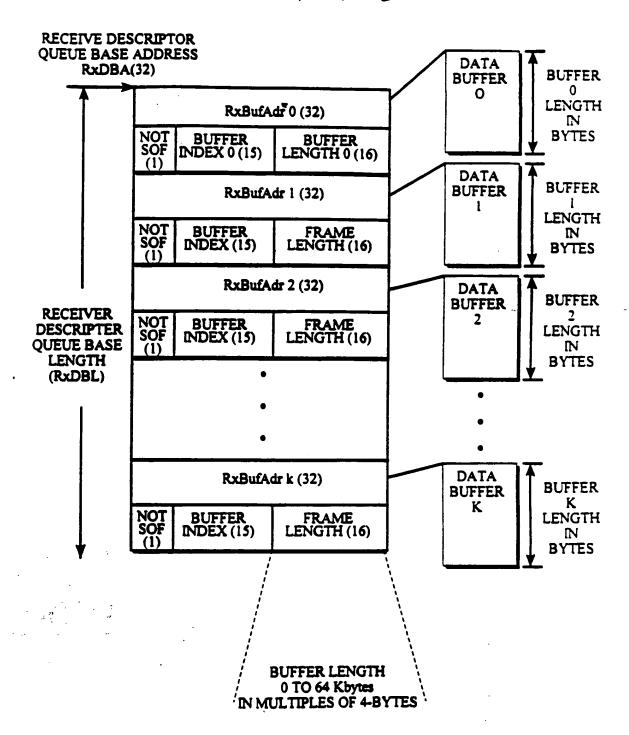


Fig. 5E

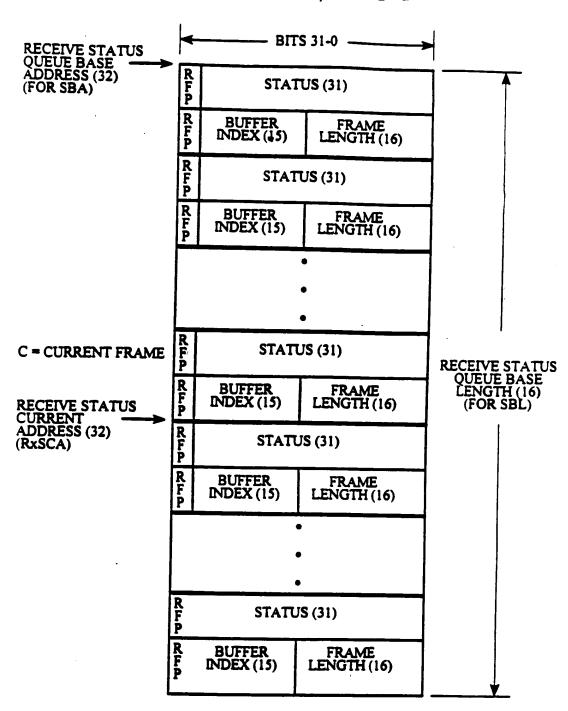
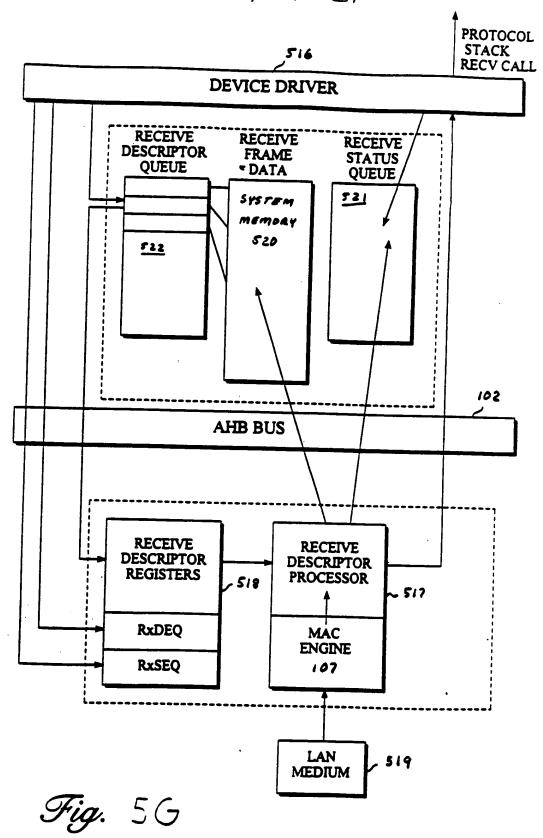
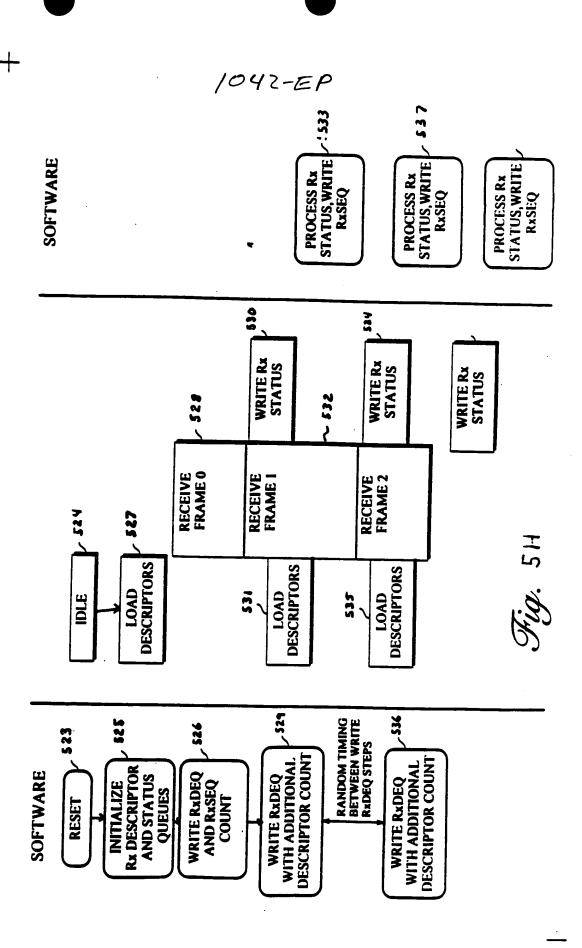
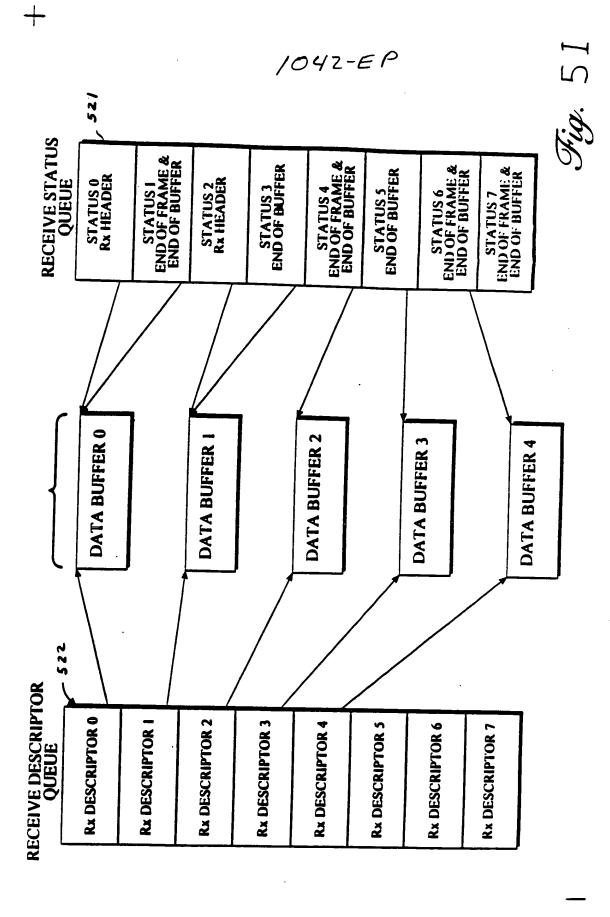


Fig. 5F



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INCOMING FRAME

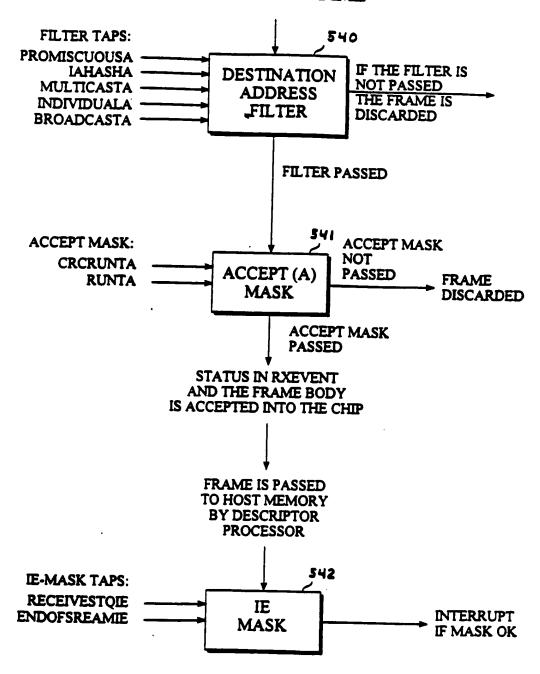
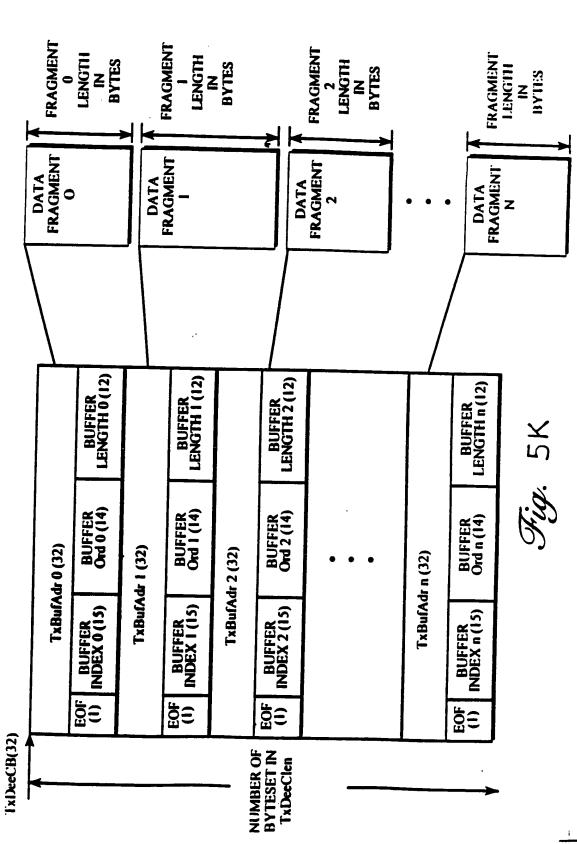


Fig. 51

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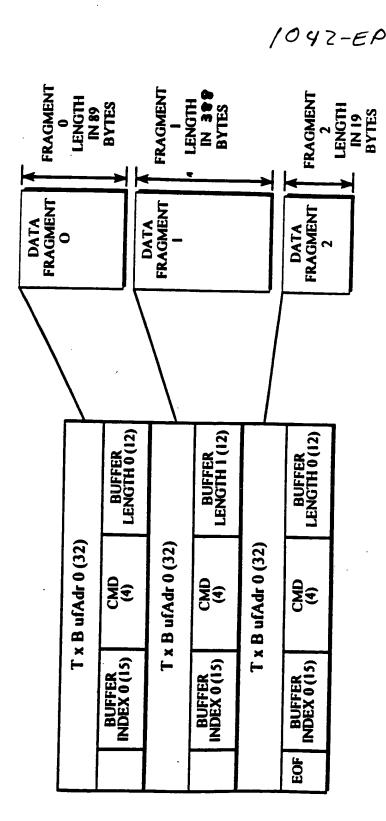
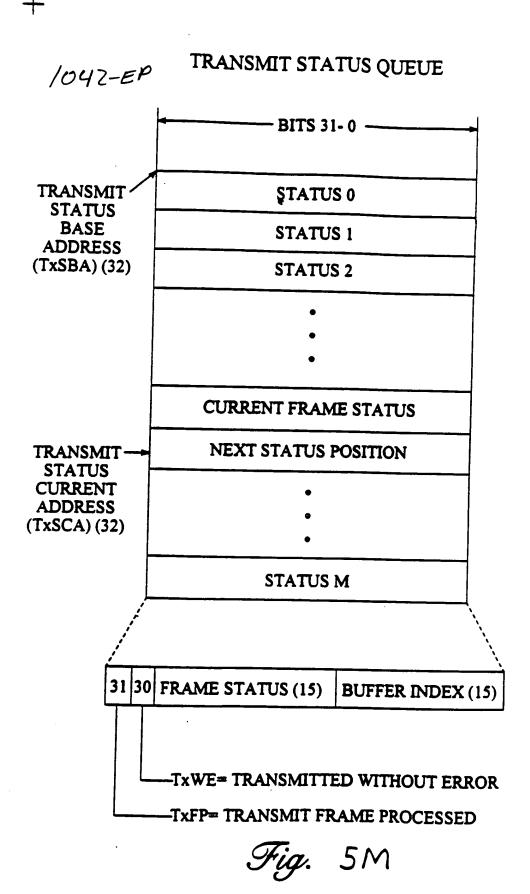
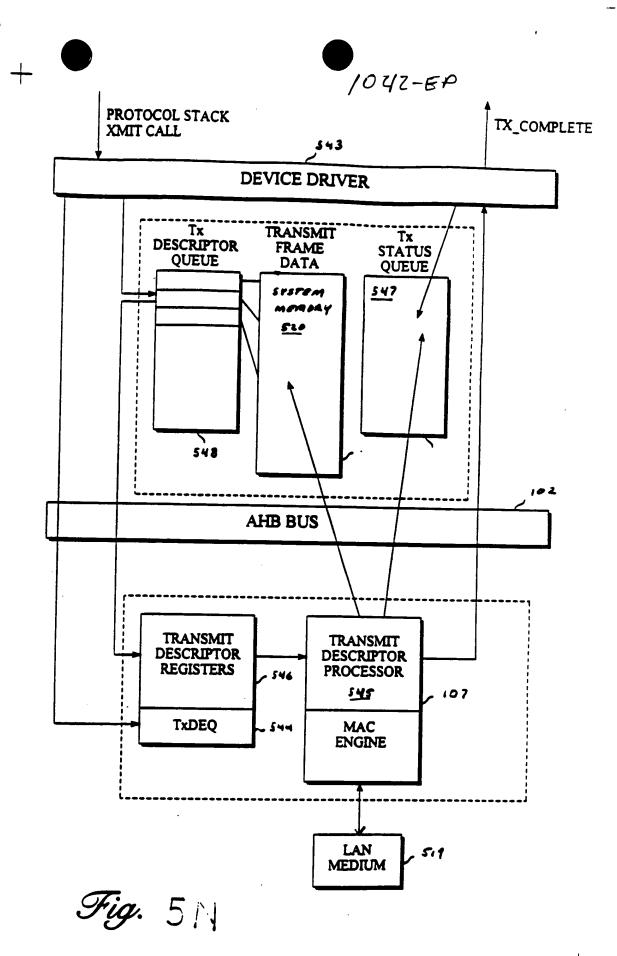
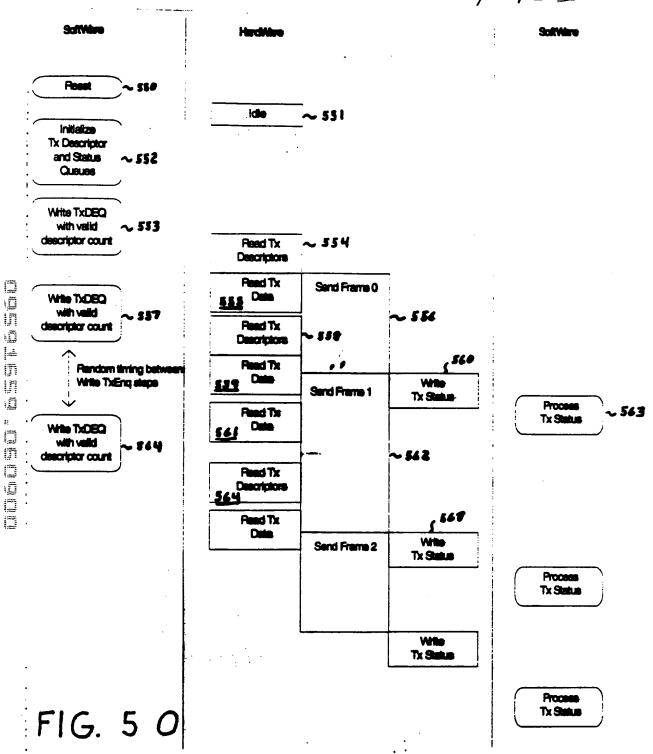


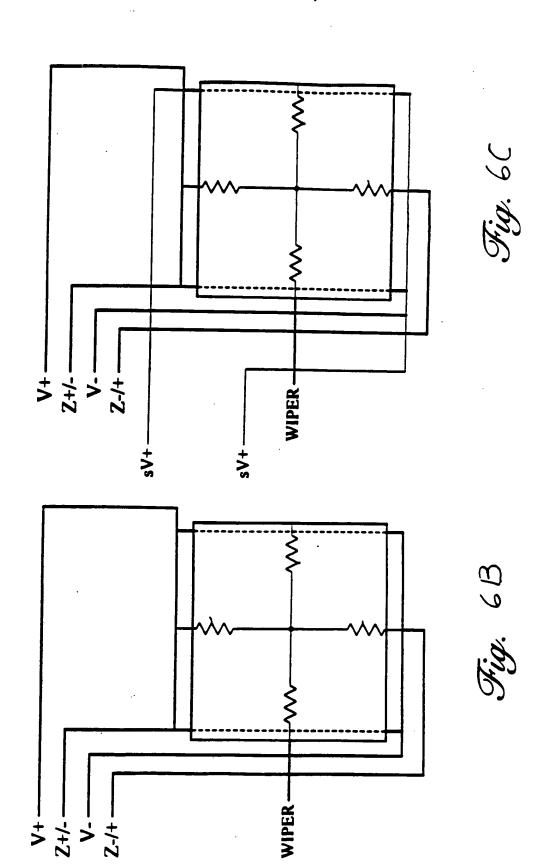
Fig. 5L

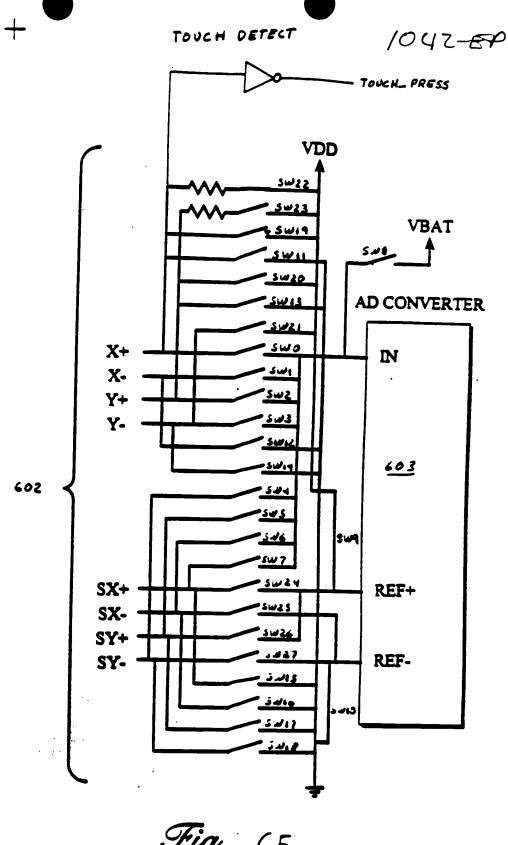




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SAMPLE X-AXIS

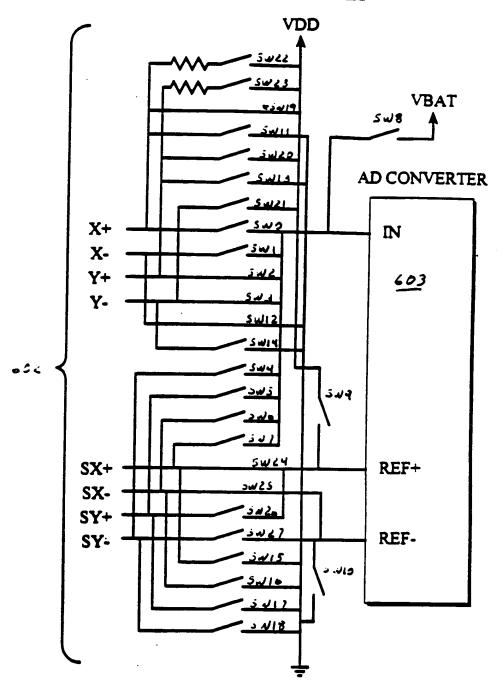


Fig. 6F

SAMPLE Y-AXIS

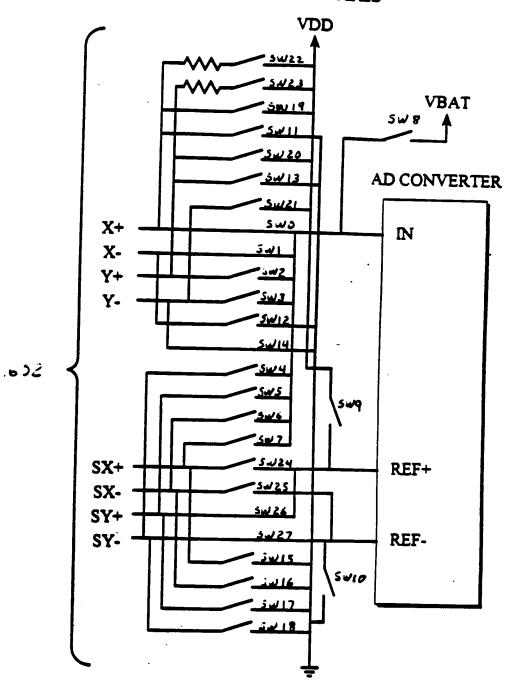


Fig. 66

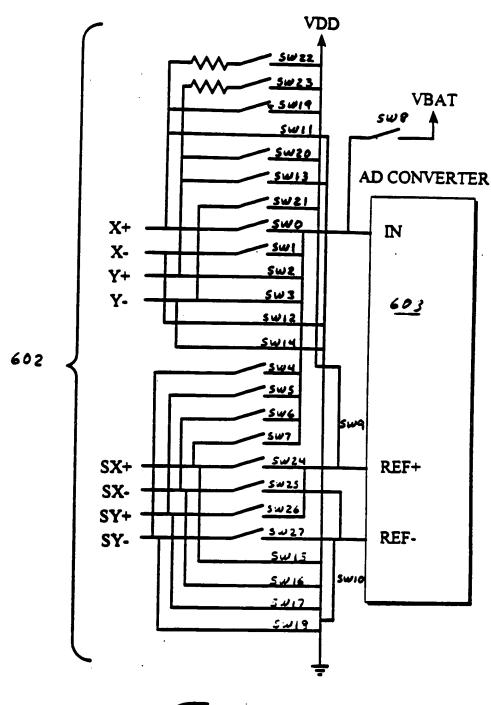
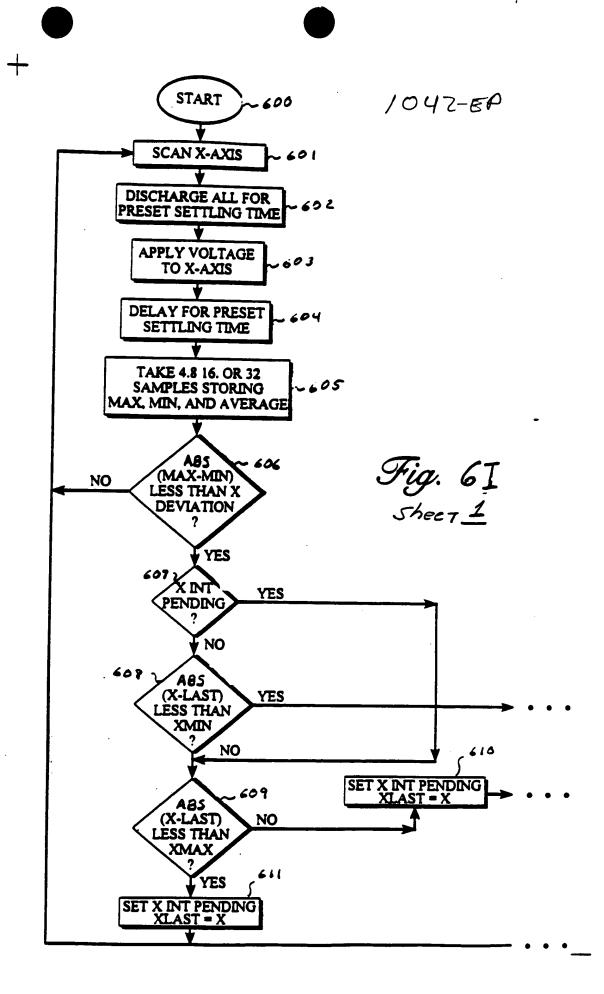
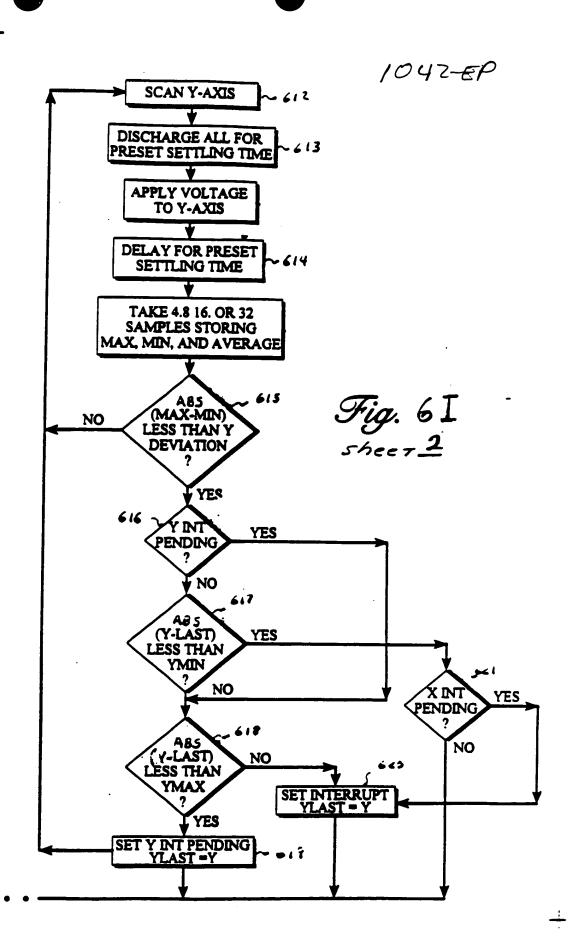
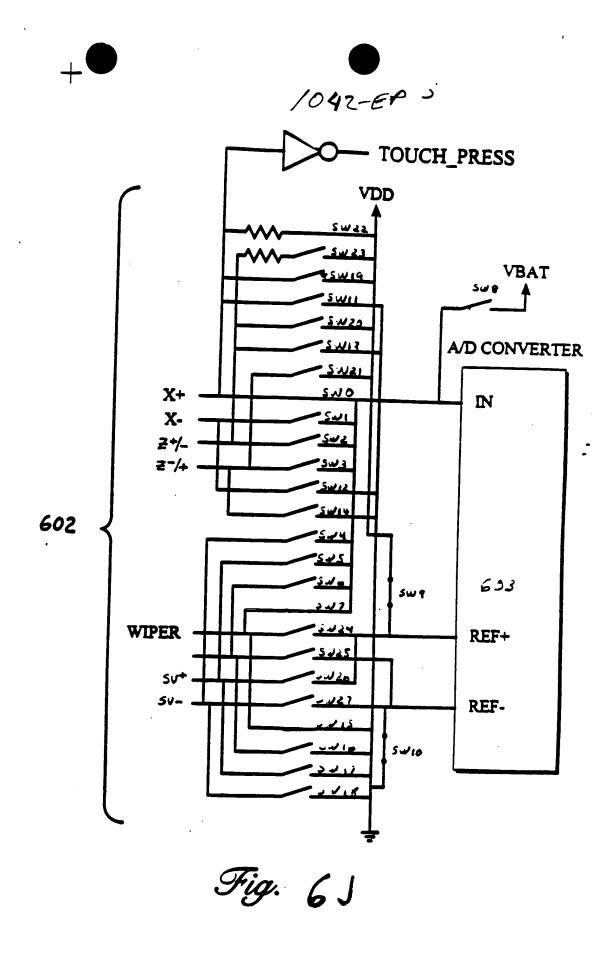


Fig. 6H







SAMPLE X-AXIS

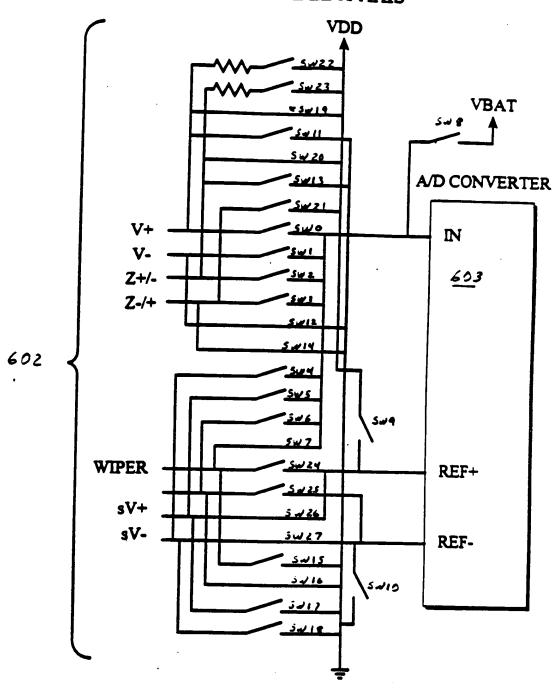


Fig. 6K

SAMPLE Y-AXIS

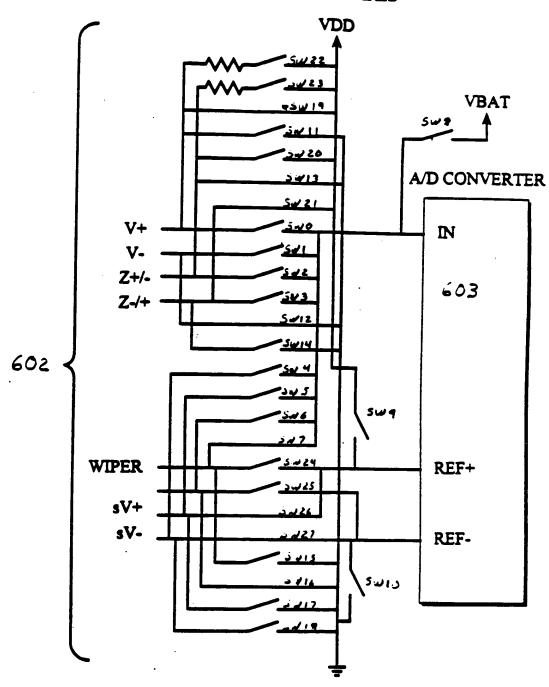


Fig. 6L

DISCHARGE ALL LINES

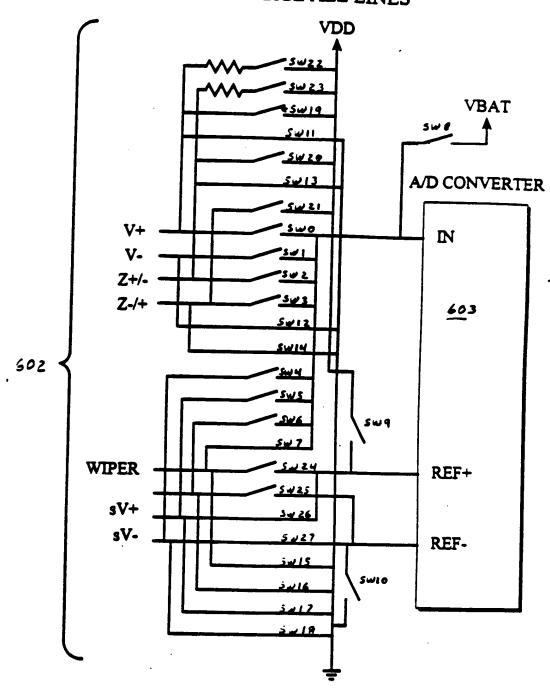


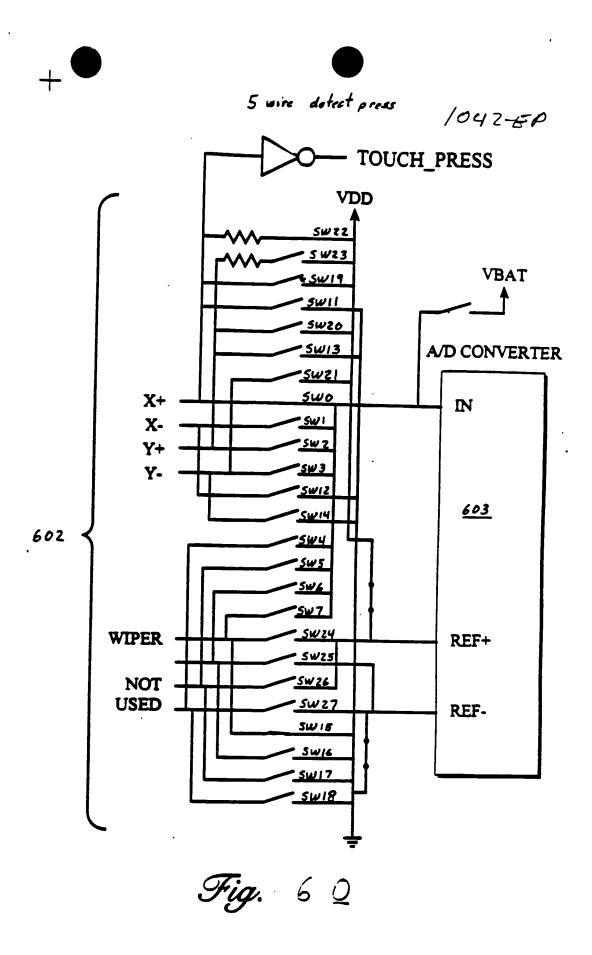
Fig. 6M

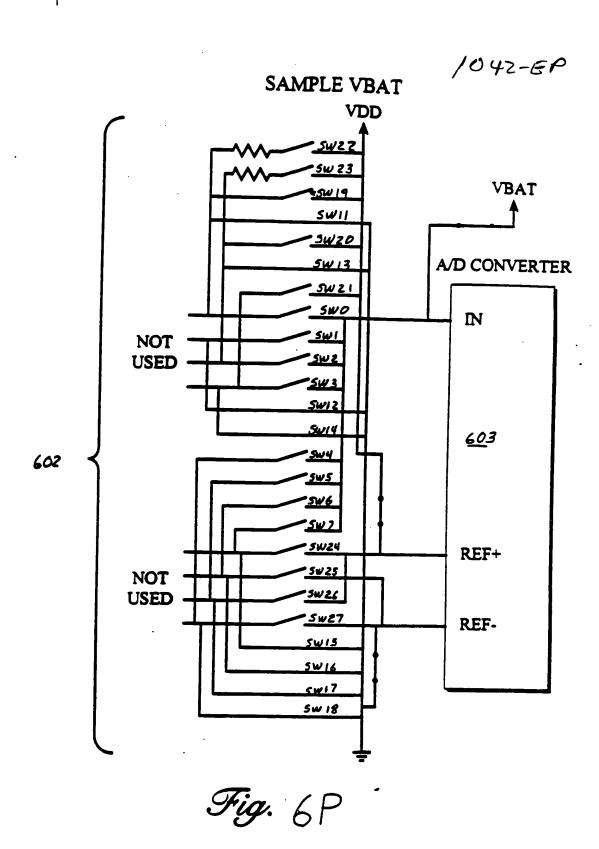
Sheer & Fig. 6NI

MINMAX DEVIATION REGISTER 57-5 YMAX REGISTER G COMP 2 MUX MUX ACCUMULATE AND SHIFT REGISTER MIN SAMPLE REGISTER MAX SAMPLE REGISTER F79 57.0 2 ADD 2 OUT A/D CONVERTER 6.5. REF+ REF. Z

Fig. GN.

DANGESS COOL





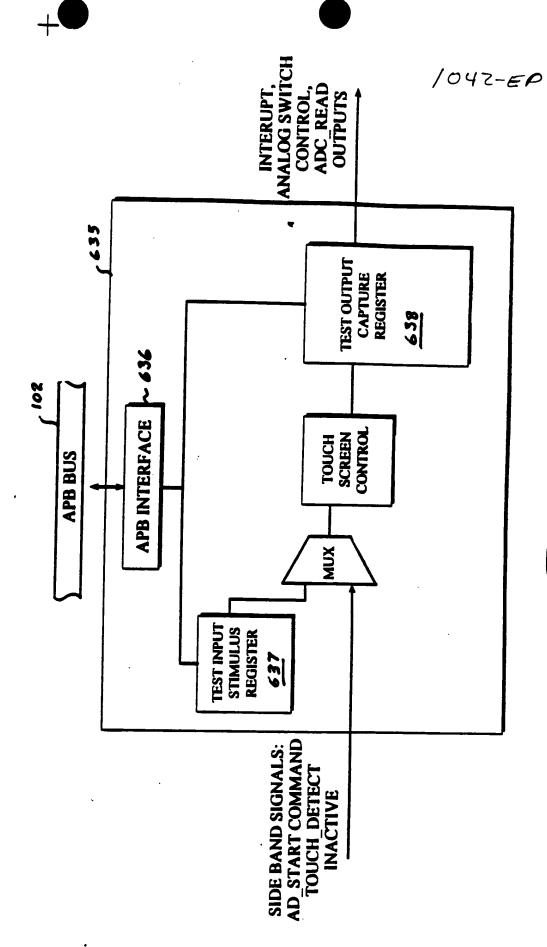


Fig. 6Q

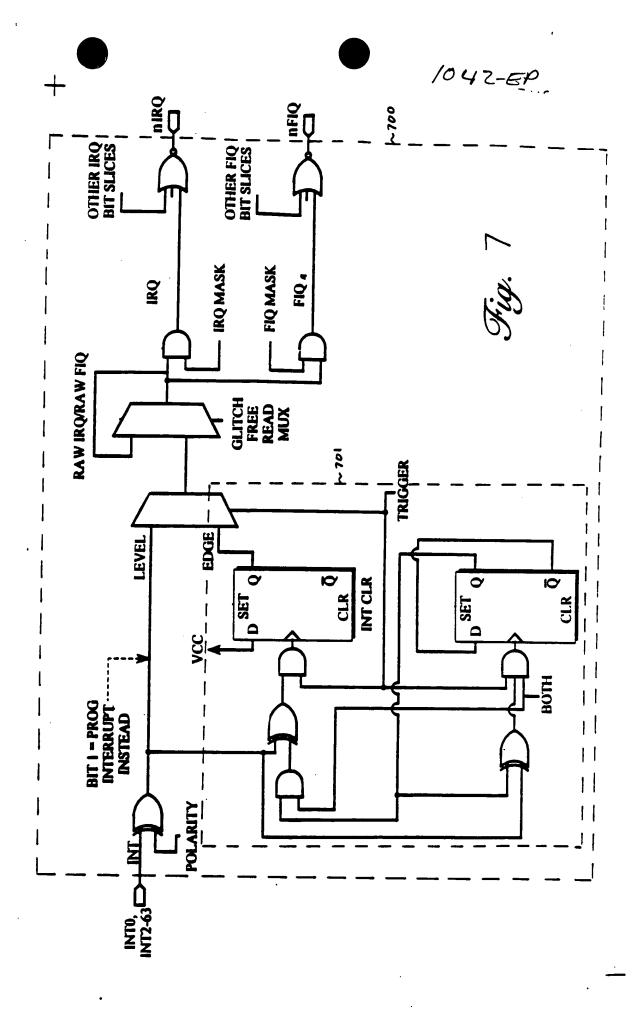
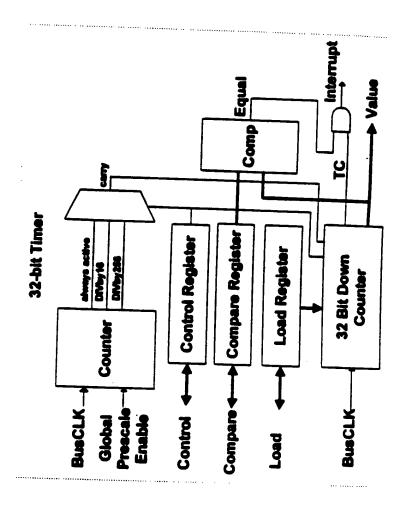


Fig. 8A

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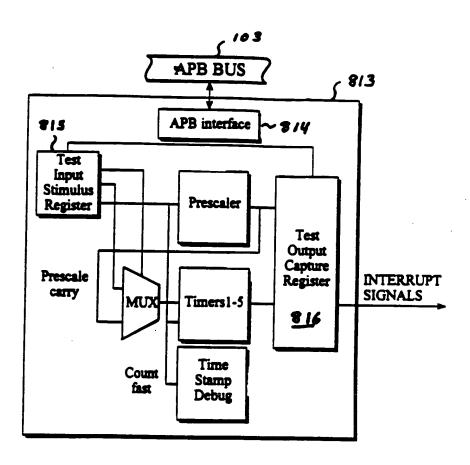
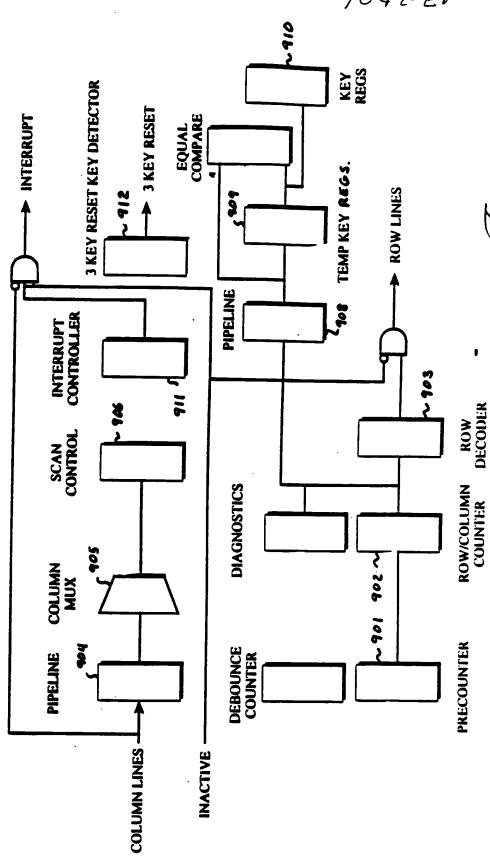


Fig. 8C



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Fig. 9A

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Fig. 9B

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ri ig	5

8x8 KEY ARRAY

			,	. – 0
	KEY 27H	КЕУ 2FH	КЕУ 37Н	KEY 3FH
•••	KEY 26H	KEY 2EH	KEY 36H	КЕУ ЗЕН
	KEY 25H	KEY 2DH	KEY 35H	KEY 3DH
	KEY 24H	KEY 2CH	KEY 34H	KEY 3CH
• • • •	KEY 23H	KEY 2BH	KEY 33H	KEY 3BH
• • • •	KEY 22H	KEY 2AH	KEY 32H	KEY 3AH
•••	KEY 21H	KEY 29H	KEY 31H	KEY 39H
•••	KEY 20H	KEY 28H	KEY 30H	KEY 38H
• • •	ROW 4	ROW 5	ROW 6	ROW 7

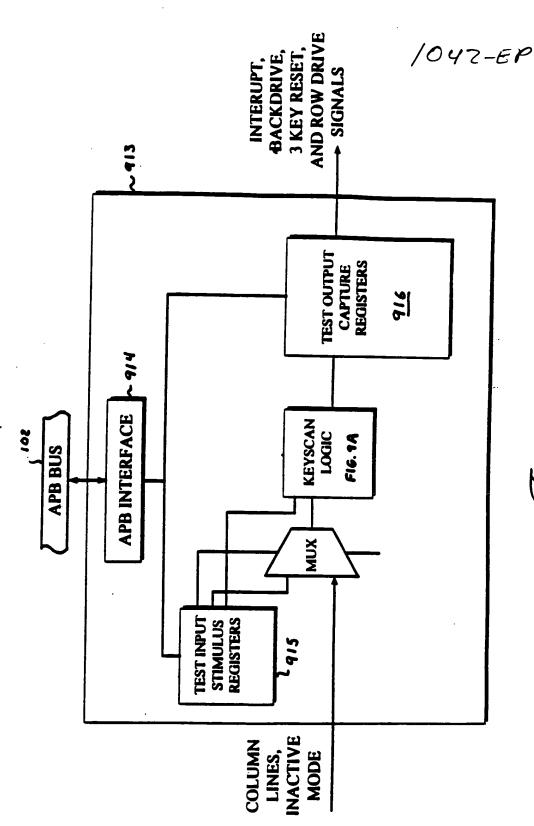
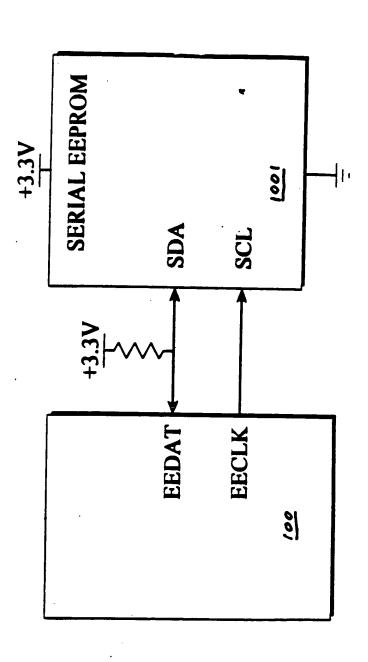


Fig. 4C



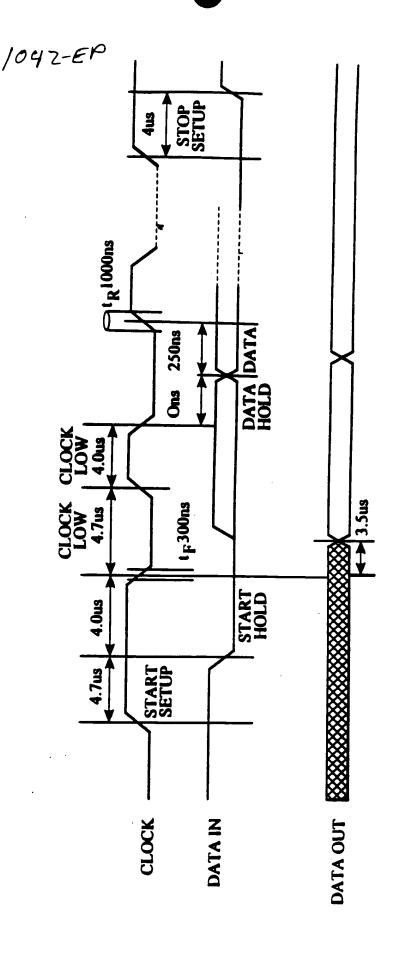
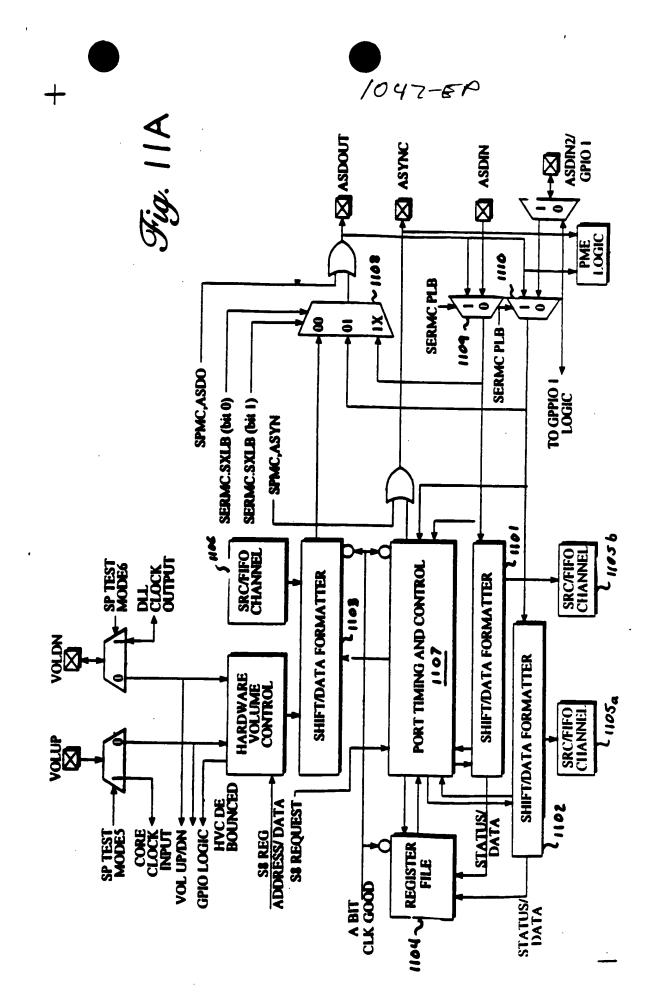
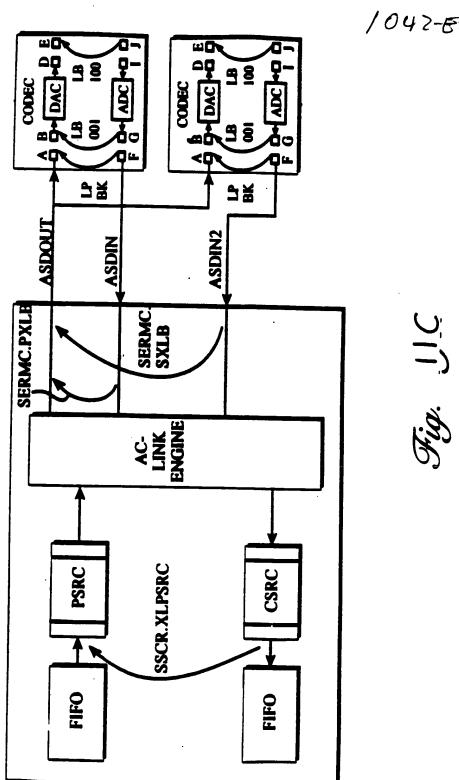


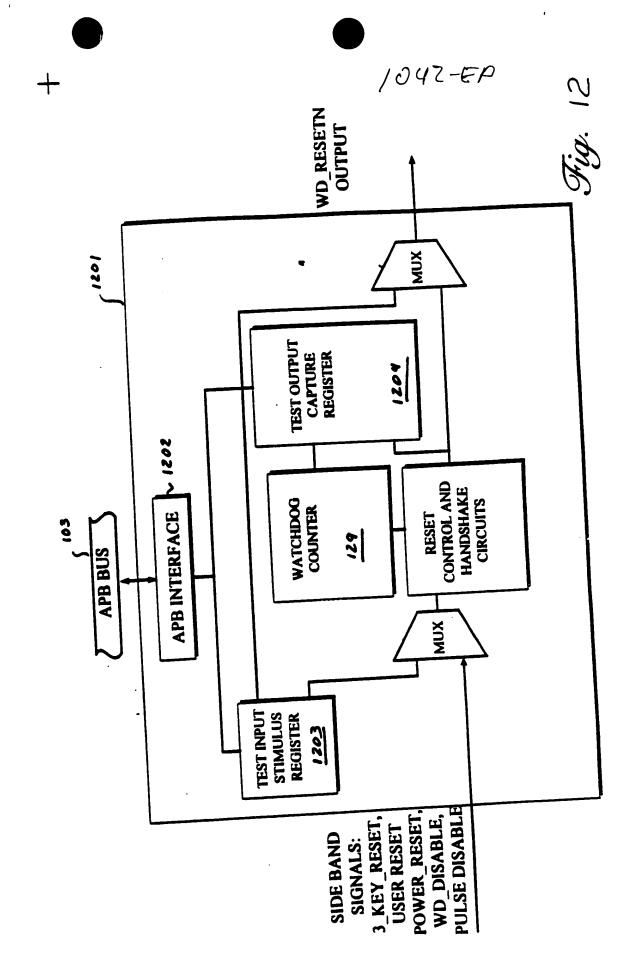
Fig. 10B

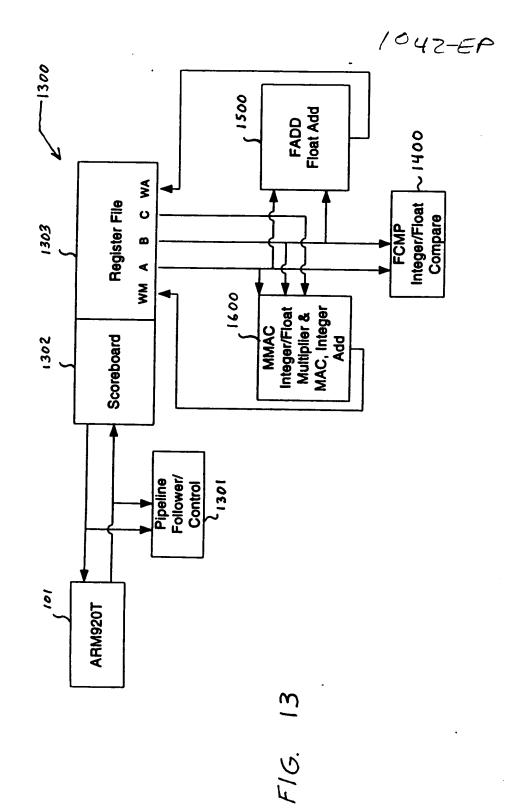


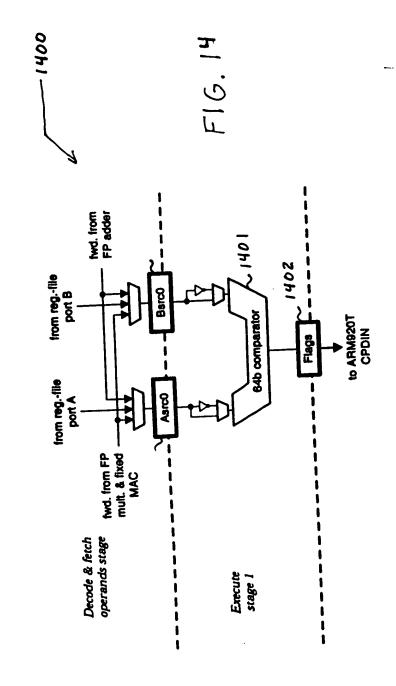
ngs.d.d.d.d.

Fig. 118





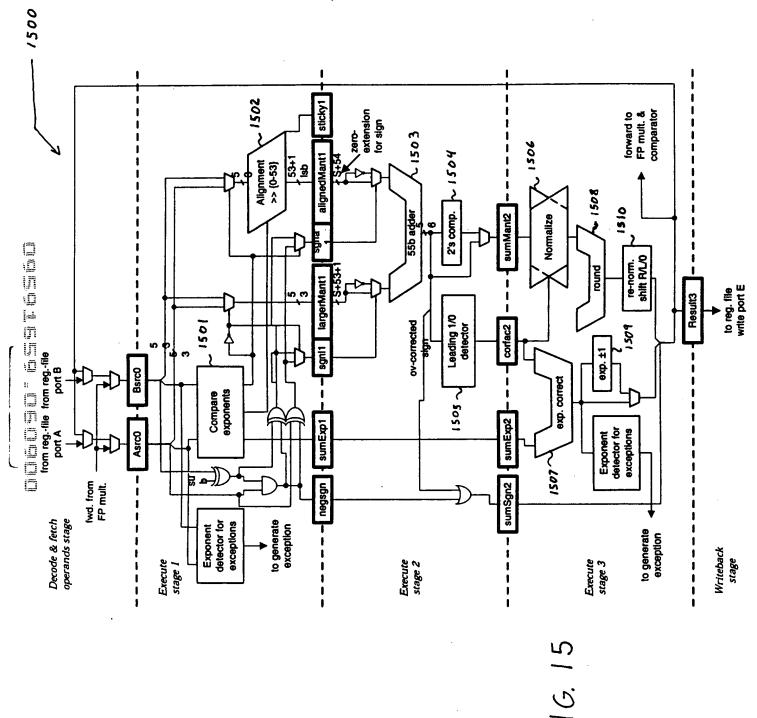




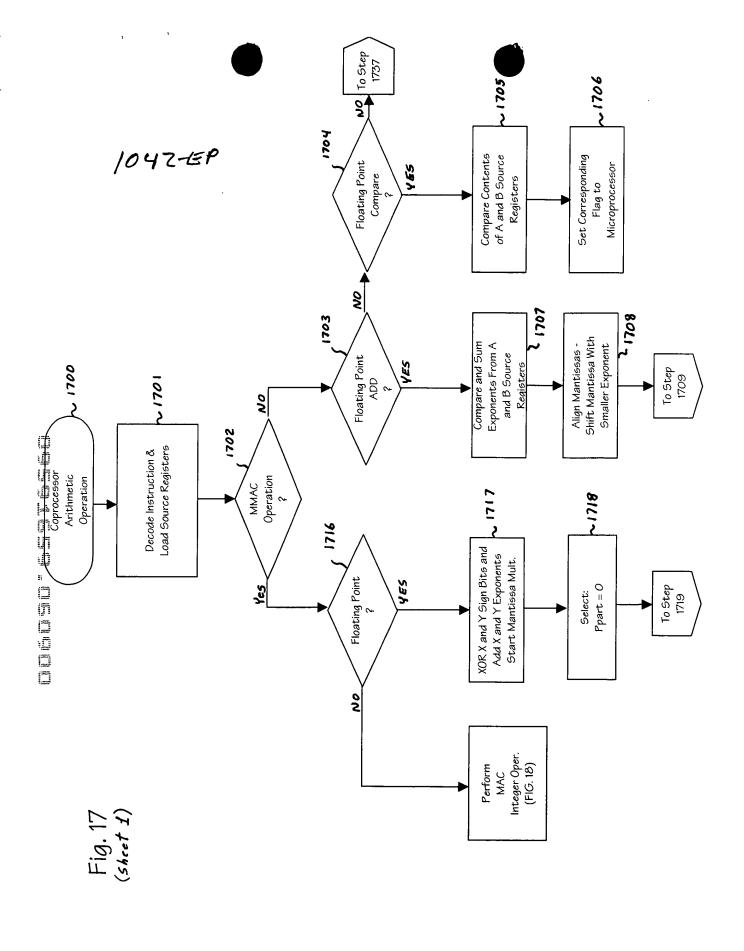
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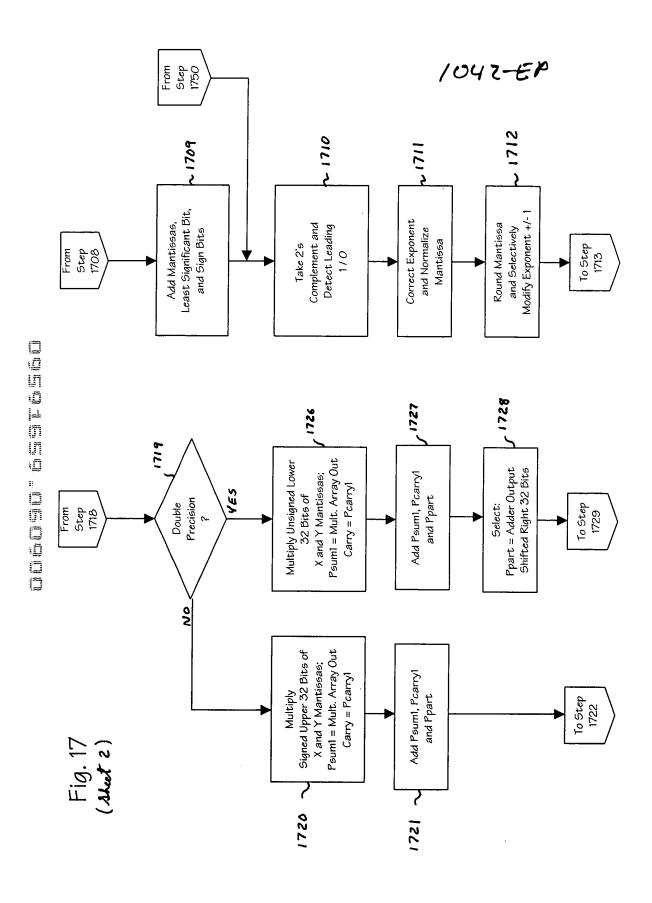
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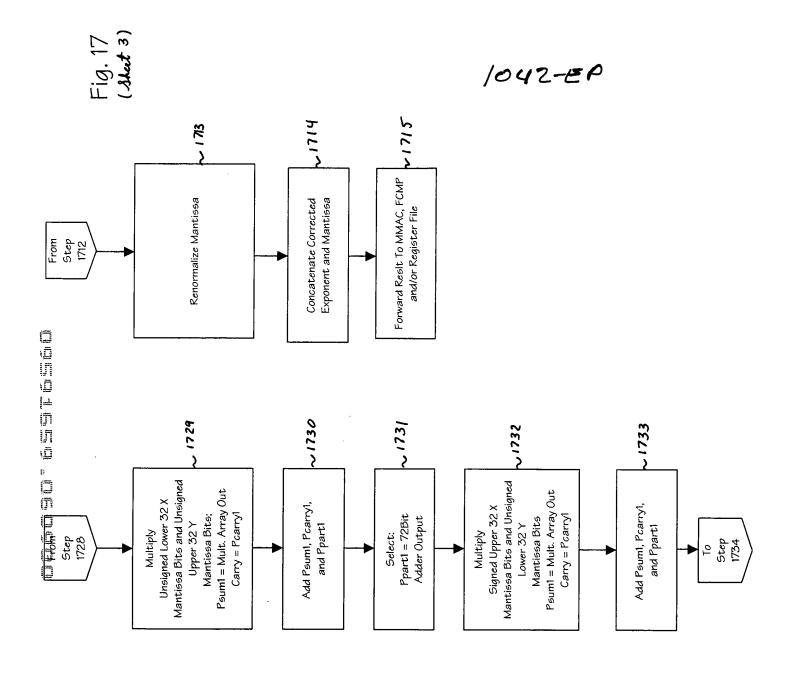
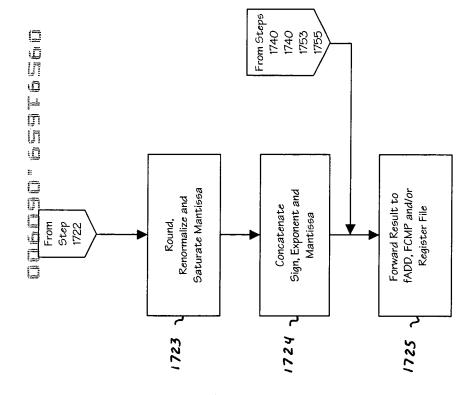


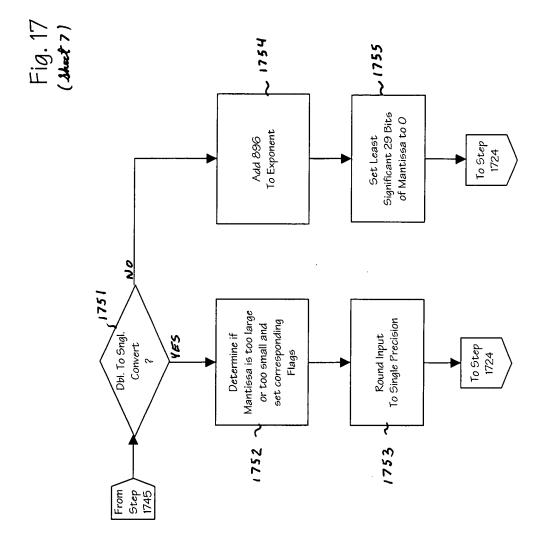
Fig. 17 (Shut 4)

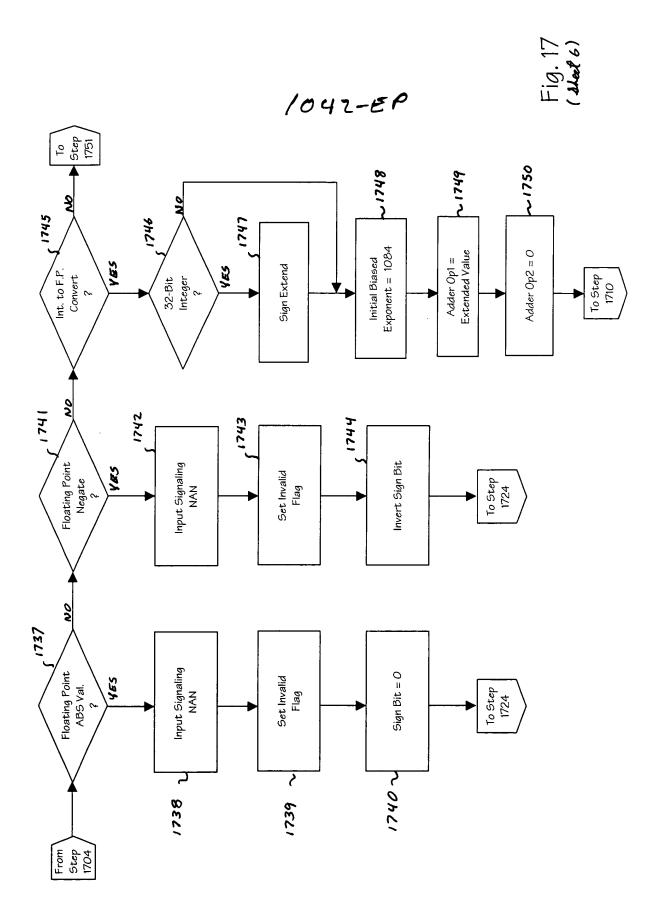
Fig. 17 (shuts)

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